

## REMARKS

It is asserted in the Office Action the Application is directed to the following separate and distinct embodiments, as follows:

- I. Claim 1-4 and 14-18, drawn to address generator and command information IC chip tester, classified in class 702, subclass 117.
- II. Claims 5-8 and 9-13, drawn to data tester with separate memory core and address decoder, classified in class 702, subclass 122.
- III. Claims 19-21, drawn to symbol IC self looping test within memory module, classified in class 702, subclass 124.

In response, Applicants have elected prosecution on Embodiment II (claims 5-8 and 9-13) without traverse. Applicants cancel Embodiment I (claims 1-4 and 14-18) and Embodiment II (claims 19-21).

Respectfully submitted,

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By:   
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### CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, Post Office Box 1450, Alexandria, Virginia 22313-1450 on December 17, 2004.

  
Margaux Rodriguez December 17, 2004